



The Integrated Systems Centre has organized a day of presentations and discussion about the future of Electrical Engineering as a discipline and profession with a group of world-renowned leaders and innovators. Much of the future of this discipline depends on how we become aware of the current challenges and opportunities and react to them.

This meeting is organized as a forum, with ample time for the audience to interact and ask questions. The forum is of interest to professors, research and teaching staff and graduate students in EE and neighbor disciplines, such as microtechniques, computer science and communication systems.

The forum is open to everyone, but for planning reasons please send an e-mail to anil.leblebici@epfl.ch to confirm your participation.



The Future of Electrical Engineering

17 December 2009, EPFL Polydôme

10:00	Giovanni De Micheli <i>EPFL Switzerland</i>	Electrical Engineering at a Crossroad: Challenges and Opportunities
10:45	Jan Rabaey <i>UC Berkeley, USA</i>	The "Meaning" of Electrical Engineering
11:30	Heinrich Meyr <i>RWTH Aachen, Germany</i>	A SWOT Analysis of Electrical Engineering
12:30-14:00	LUNCH BREAK	
14:00	Mark Horowitz <i>Stanford University, USA</i>	Electrical Engineering: Where the Physical World meets the Virtual World
14:45	Satoshi Goto <i>Waseda University, Japan</i>	Ambient SoC Initiative
15:30	Lothar Thiele <i>ETHZ Switzerland</i>	Towards Electrical Engineering - Again
16:15-17:00	Discussion	

Please refer to <http://si.epfl.ch> for more info.